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PATENT NUMBER

4

U.S. **UTILITY** Patent Application

**O.I.P.E.**

**PATENT DATE**

O.I.P.E.  
SCANNED *hpcc3* Q.A. *41*

APPLICATION NO. 09/900945	CONT/PRIOR F	CLASS 714	SUBCLASS -	ART UNIT 2184	EXAMINER L. Leach
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## APPLICANTS

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515

2100  
2133

## TRIMMINGS

**TITLE**

One-chip system large-scale integrated circuit including processor circuit and its peripheral circuits

PTO-2040  
12/99

ISSUING CLASSIFICATION												
ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.  <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____    	_____ (Assistant Examiner) (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
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<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		<b>ISSUE BATCH NUMBER</b>	
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